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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application / Conf. No.	10/791,924 / 3818
	Filing Date	March 02, 2004
	First Named Inventor	Brian T. Brunn
	Examiner Name	Siu M. Lee
	Art Unit	2611
	Patent No.	
Mail Stop: AMENDMENT	Attorney Docket Number	X-1549 US
Express Mail Receipt No.		
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X-1549 US
10/791,924



PATENT
Conf. No.: 3818

UNITED STATES PATENT OFFICE

Applicant: Brian T. Brunn et al.

Assignee: Xilinx, Inc.

Title: Bit-Edge Zero Forcing Equalizer

Serial No.: 10/791,924

File Date: March 2, 2004

Examiner: Siu M. Lee

Art Unit: 2611

Docket No.: X-1549

Conf. No.: 3818

Mail Stop Amendment
COMMISSIONER FOR PATENTS
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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

In response to the First Office Action mailed from the Patent Office on June 12, 2007, please replace/substitute the following claim(s) as indicated.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 6 of this paper.

Amended Replacement Drawing Sheets are included as attachments to this paper.